

	Type	L #	Hits	Search Text	DBs
5	BRS	L5	1	(adjustment same register with counter same (pulse adj width adj modulat\$3 PWM) same cycle adj count and adder with register).clm.	US-PGPUB
6	BRS	L6	1	(first adj value same second adj value same working adj cycle same duty adj cycle with averag\$3).clm.	US-PGPUB

Application  
Number 

IDS Flag Clearance for Application 10532821

**IDS**  
Information

Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
M844	2005-11-08	34	Y <input checked="" type="checkbox"/>	2007-03-20 12:20:14.0	LNguyen10
<input type="button" value="Update"/>					